



SSCN143GN1

NPN Type Digital Transistor (built-in resistors)

➤ Features

VCC	VIN	IO	R1	R2/R1 Typ.
50V	-5~+30V	100mA	4.7kΩ	10

➤ Description

Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).

The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects. Only the on/off conditions need to be set for operation, making the device design easy.

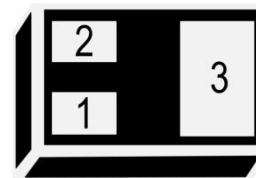
➤ Applications

- Amplifying signal
- Electronic switch
- Oscillating circuit
- Variable resistance

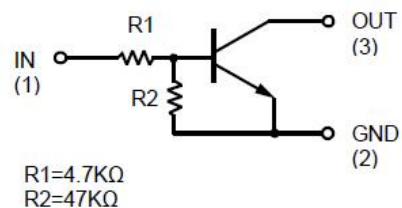
➤ Ordering Information

Device	Package	Shipping
SSCN143GN1	DFN1006-3L	10000/Reel

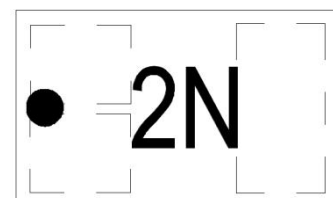
➤ Pin configuration



DFN1006-3L



Circuit Diagram



Marking(Top View)



➤ **Absolute Maximum Ratings**($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Supply Voltage	V_{CC}	50	V
Input Voltage	V_{IN}	-5 to +30	V
Output current	I_o	100	mA
Collector Power Dissipation	P_D	150	mW
Junction Temperature	T_J	-55 to 150	$^{\circ}\text{C}$
Storage Temperature	T_{STG}	-55 to 150	$^{\circ}\text{C}$

➤ **Electrical Characteristics** ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Input Voltage	$V_{I(off)}$	$V_{CC}=5V$, $I_o=100\mu A$	0.5			V
	$V_{I(on)}$	$V_{CC}=0.3V$, $I_o=5mA$			1.3	V
Output Voltage	$V_{O(on)}$	$I_o/I_i=5mA/0.25mA$		0.1	0.3	V
Input Current	I_i	$V_i=5V$			1.8	mA
Output Current	$I_{O(off)}$	$V_{CC}=50V$, $V_i=0V$			0.5	μA
DC Current Gain	G_1	$V_o=5V$, $I_o=10mA$	80			
Input Resistance	R_1		3.29	4.7	6.11	$K\Omega$
Resistance Ration	R_2/R_1		8	10	12	$K\Omega$
Transition Frequency	f_T	$V_{CE}=10V, I_E=-5mA, f=100MHz$		250		MHz

➤ Typical Performance Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

Fig.1 Input voltage vs. output current (ON characteristics)

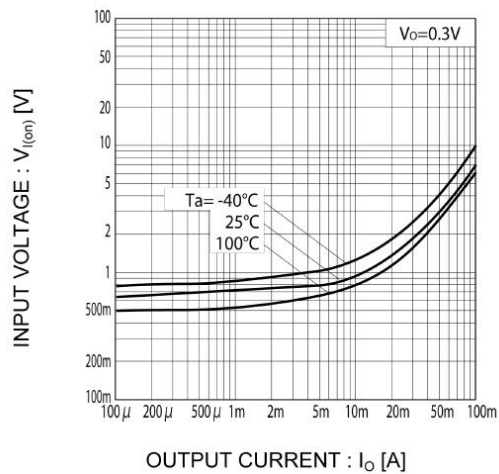


Fig.2 Output current vs. input voltage (OFF characteristics)

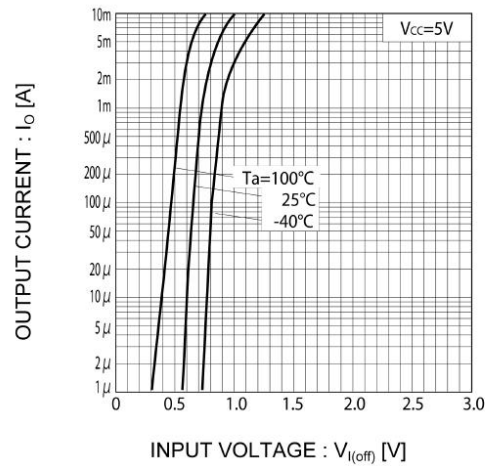


Fig.3 Output current vs. output voltage

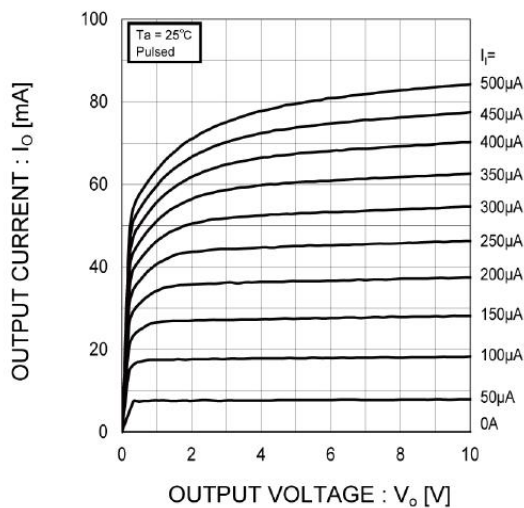


Fig.4 DC current gain vs. output current

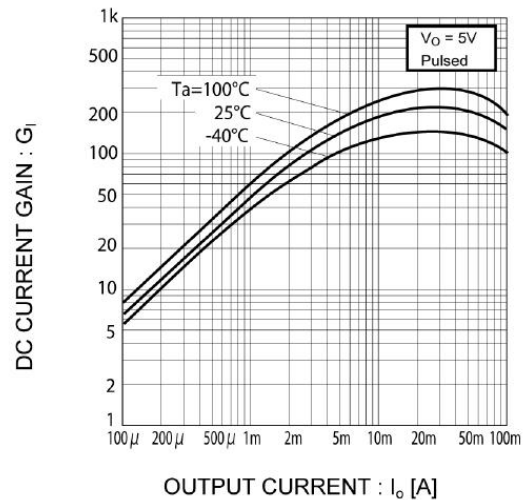
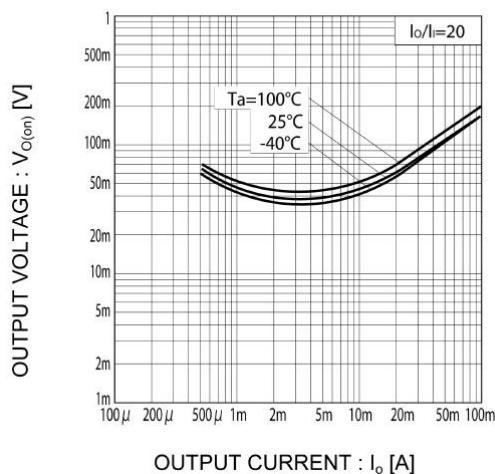


Fig.5 Output voltage vs. output current

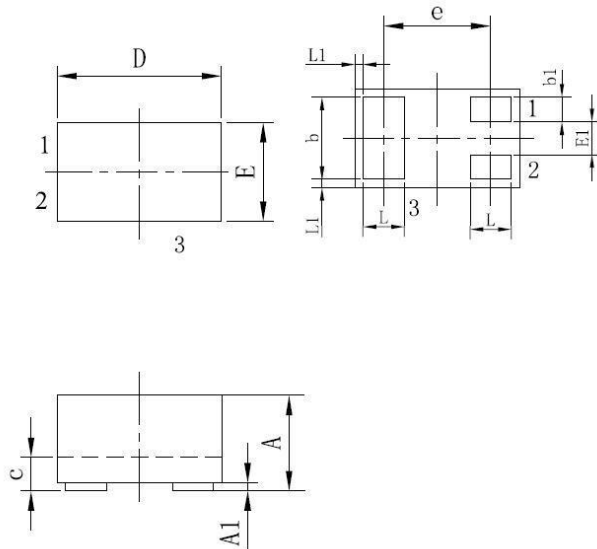




- **Package Information**
- **Mechanical Data**

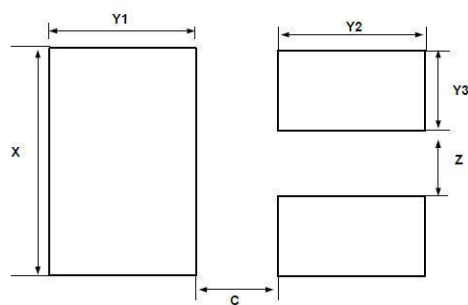
Case: DFN1006-3L

Case Material: Molded Plastic. UL Flammability



DIM	Millimeters		
	Min	Nom	Max
A	0.45	0.50	0.55
A1	0.00	0.02	0.05
b	0.45	0.50	0.55
b1	0.10	0.15	0.20
c	0.12	0.15	0.18
D	0.95	1.00	1.05
e	0.65 BSC		
E	0.55	0.60	0.65
E1	0.15	0.20	0.25
L	0.20	0.25	0.30
L1	0.05REF		

- **Suggested Pad Layout**



DIM	Millimeters
C	0.25
X	0.65
Y1	0.50
Y2	0.50
Y3	0.25
Z	0.20



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